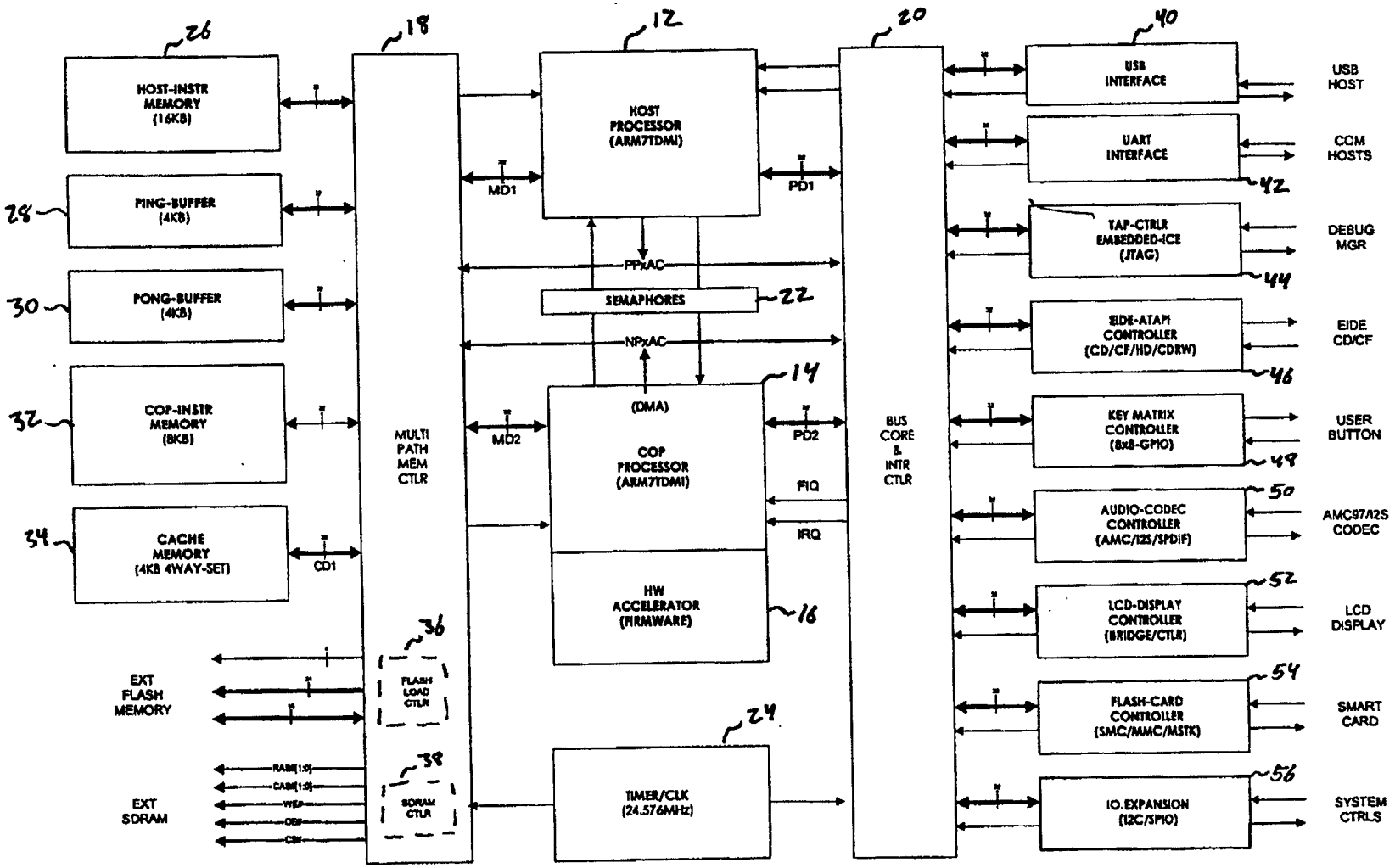
10
26
HOST-INSTR MEMORY (16KB)
28
PING-BUFFER (4KB)
30
PONG-BUFFER (4KB)
32
COP-INSTR MEMORY (8KB)
34
CACHE MEMORY (4KB 4WAY-SET)
CD1
18
MULTI PATH MEM CTLR
MD2
36
FLASH LOAD CTLR
EXT FLASH MEMORY
38
SDRAM CTLR
EXT SDRAM
RAS#[1:0]
CAS#[1:0]
WE#
OE#
CS#
12
HOST PROCESSOR (ARM7TDMI)
MD1
PD1
PPxAC
SEMAPHORES
22
NPxAC
14
(DMA)
COP PROCESSOR (ARM7TDMI)
PD2
FIQ
IRQ
HW ACCELERATOR (FIRMWARE)
16
24
TIMER/CLK (24.576MHz)
20
BUS CORE & INTR CTLR
40
USB INTERFACE
USB HOST
UART INTERFACE
COM HOSTS
42
TAP-CTRLR EMBEDDED-ICE (JTAG)
DEBUG MGR
44
EIDE-ATAPI CONTROLLER (CD/CF/HD/CDRW)
EIDE CD/CF
46
KEY MATRIX CONTROLLER (8x8-GPIO)
USER BUTTON
48
50
AUDIO-CODEC CONTROLLER (AMC/I2S/SPDIF)
AMC97/I2S CODEC
52
LCD-DISPLAY CONTROLLER (BRIDGE/CTLR)
LCD DISPLAY
54
FLASH-CARD CONTROLLER (SMC/MMC/MSTK)
SMART CARD
56
IO.EXPANSION (I2C/SPIO)
SYSTEM CTRLS

Figure 1

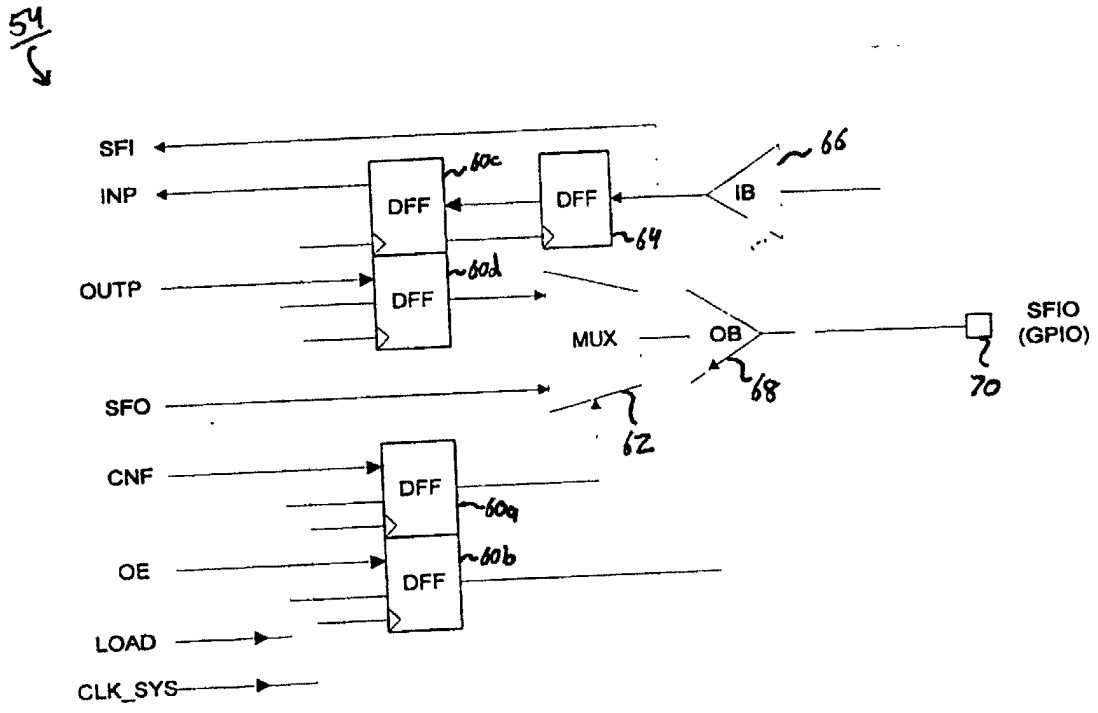
54
SFI
INP
OUTP
SFO
CNF
OE
LOAD
CLK_SYS
DFF
DFF
DFF
DFF
DFF
60c
64
60d
MUX
OB
IB
66
68
62
SFIO
(GPIO)
70
60a
60b

Figure 2

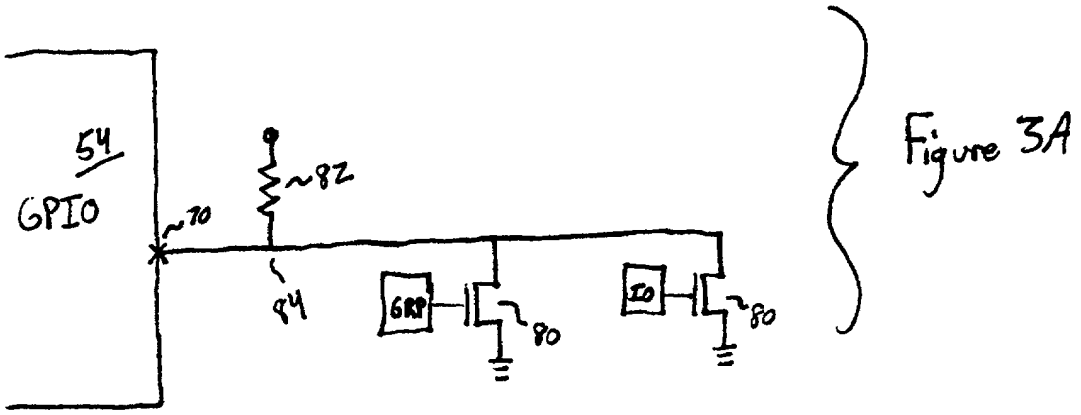

Figure 3A

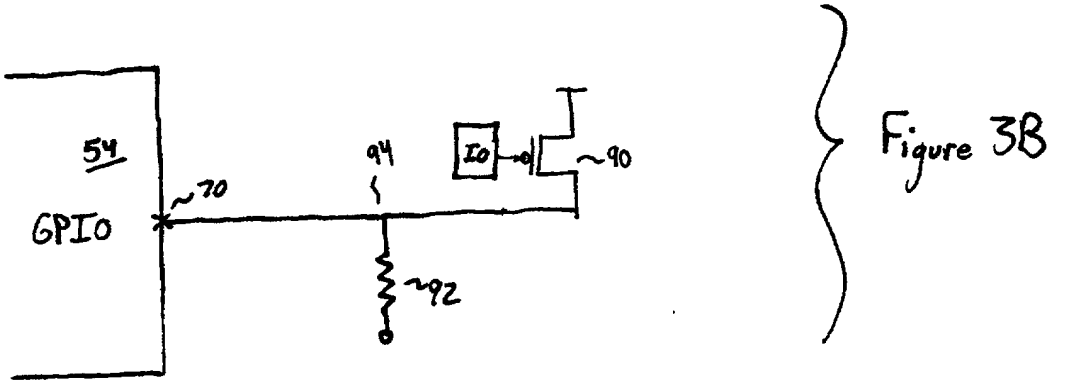

Figure 3B

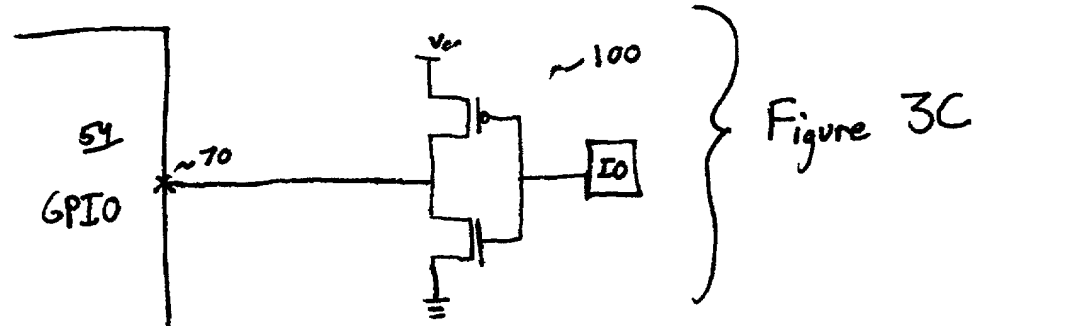

Figure 3C

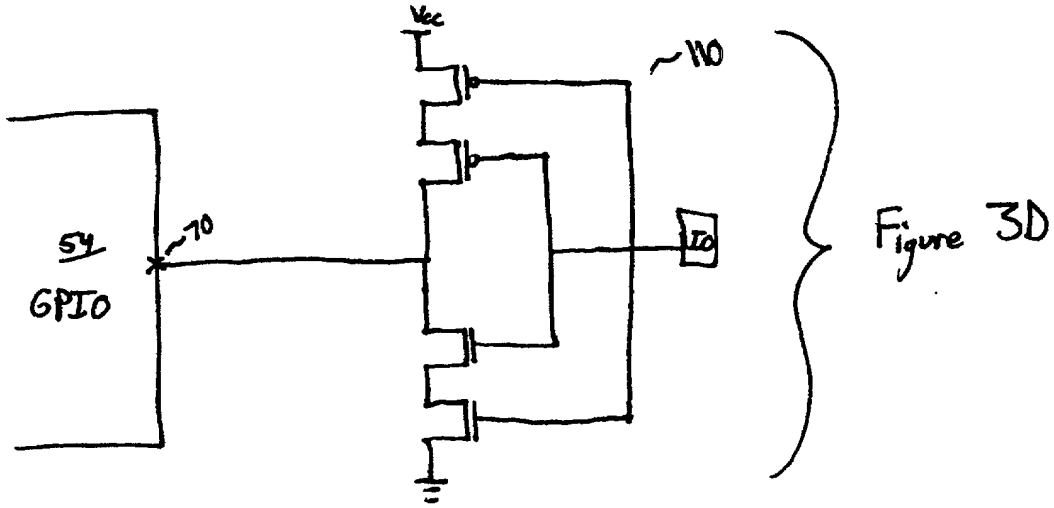

Figure 3D

T020S0" E96Z4360

	CFG-REG NAME	ADDRESS	TYPE	SIZE	DISCRIPTION	7 6 5 4 3 2 1 0
	GPIO Controller					
120	GPIO_CNF	CF00:0000	R/W	8b	GPIO/SPIO selection	PORT-A.ENB
122	GPIO_OE	CF00:0010	R/W	8b	GPIO Output Enable	PORT-A.OE
124	GPIO_OUT.REG	CF00:0020	R/W	8b	GPIO Output Port Register	PORT-A.OUT
126	GPIO_IN.REG	CF00:0030	R/O	8b	GPIO Input Port Register	PORT-A.IN
128	GPIO_INT.STA	CF00:0040	R/O	8b	GPIO Interrupt Status Register	PORT-A.INT.STA
130	GPIO_INT.ENB	CF00:0050	R/W	8b	GPIO Interrupt Enable Register	PORT-A.INT.ENB
132	GPIO_INT.LVL	CF00:0060	R/W	8b	GPIO Interrupt Activation Level	PORT-A.INT.LVL
134	GPIO_INT.CLR	CF00:0070	clr	8b	GPIO Interrupt Flag Set-to-Clear	PORT-A.INT.CLR

Figure 4